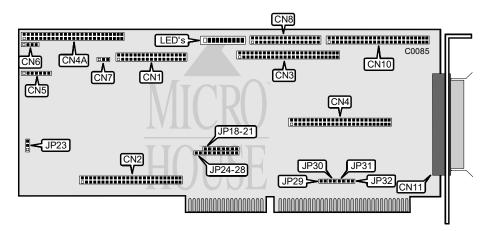
DISTRIBUTED PROCESSING TECHNOLOGY PM2011/90, PM2011/95

Data bus: 16-bit, ISA

Size: Full-length, full-height card
Hard drives supported: Up to seven SCSI devices
Floppy drives supported: PM2011/90: None

PM2011/95: Two 360KB, 720KB, 1.2MB or 1.44MB drives

Sector interleave: 1:



CONNECTIONS	
Function	Location
Memory module connector	CN1
Memory module connector	CN2
Cache module connector	CN3
Cache module connector	CN4
Memory Expansion Board Cable Connector	CN4A
Disk mirroring module connector	CN5
4-pin connector-drive active LED	CN6
Disk mirroring module connector	CN7
50-pin SCSI connector-internal	CN10
50-pin SCSI connector-external	CN11

Continued on next page . . .

Copyright © 1991, 1992, 1993 by Micro House Int'l (800) 926-8299. Research Dept. (303) 443-389.

DISTRIBUTED PROCESSING TECHNOLOGY PM2011/90, PM2011/95

. . . continued from previous page

USER CONFIGURABLE SETTINGS			
Function	Location	Setting	
Address lines SA17-19 and LA17-19 are driven	JP19	closed	
Address lines LA17-19 only are driven	JP19	open	
í Floppy drive enabled	JP20	open	
Floppy drive disabled	JP20	closed	
í BIOS EPROM size is 256KB	JP23	pins 1 & 2 closed	
BIOS EPROM size is 512KB	JP23	pins 2 & 3 closed	
í Bus Mastering enabled	JP24	open	
Bus Mastering disabled	JP24	closed	
í Bus Master DMA speed is 4MB per second	JP27	closed	
Bus Master DMA speed is 5MB per second	JP27	open	
f Floppy drive port address is 3F0-3F7h	JP34	open	
Floppy drive port address is 3F0-3F7h	JP34	closed	

SCSI ADDRESS			
SCSI I.D.	JP25	JP18	JP26
0	open	open	open
1	closed	open	open
2	open	closed	open
3	closed	closed	open
4	open	open	closed
5	closed	open	closed
6	open	closed	closed
í 7	closed	closed	closed

MEMCS16 SIGNAL STATUS		
Status	JP21	JP28
í MEMCS16 not used (16-bit memory address only)	open	closed
MEMCS16 detected (8/16-bit memory addresses OK)	closed	open

	INTERRUPT REQUEST	
IRQ	JP29	JP30
í IRQ14	closed	closed
IRQ15	closed	open
IRQ12	open	closed
Disabled	open	open

Continued on next page . . .

Copyright © 1991, 1992, 1993 by Micro House Int'l (800) 926-8299. BBS (303) 443-9957.

DISTRIBUTED PROCESSING TECHNOLOGY PM2011/90, PM2011/95

... continued from previous page

	DMA CHANNEL	
DRQ	JP31	JP32
í DRQ/DACK5	closed	closed
DRQ/DACK6	closed	open
DRQ/DACK7	open	closed
DRQ/DACK0	open	open

	I/O PORT ADDRESS	
Address	JP33	JP35
í 1F0-1F8h	closed	closed
170-178h	open	closed
330-338h	closed	open
230-238h	open	open

	BIOS ADDRESS	
Address	JP36	JP37
í C8000h	closed	closed
D8000h	closed	open
DC000h	open	closed
Disabled	open	open

LED INFORMATION	
Busy	LED1
Computer bus transfer to controller	LED2
Computer bus transfer from controller	LED3
Cache hit	LED4
Disk read-ahead active	LED5
Disk read	LED6
Disk write	LED7
Controller reset	LED8
Controller interrupt pending to computer	LED9
DRQ asserted to computer	LED10

Copyright © 1991, 1992, 1993 by Micro House Int'l (800) 926-8299. Research Dept. (303) 443-389.